AMENDMENT TO THE SPECIFICATION

Please amend the specification at page 12, beginning at line 11 as follows:

FIG. 2 shows the halftone processor **18** operational characteristics. In this example, there is illustrated a color processing system, using four separations C(x, y), M(x, y), Y(x, y), K(x, y), obtained and each processed independently for halftoning purposes to reduce an m-bit input into an n-bit output. It will be appreciated that the invention is also applicable to the "single separation" or black and white reproduction situation as well. Accordingly, a source of screen matrix information is shown, screen matrix memory **106**, which provides one input to each comparator **100, 102, 104, ~~106~~** and **108** for each separation, where the other comparator is the m bit separation bitmap. The output is m bit output, which can be directed to a printer. This illustration is highly simplified, in that distinct screen matrices may be supplied to each comparator.